

## About the Faculty Development Program

With the technology advancement in the area of electronic integrated circuits, the existing methods of performing digital experiments using various ICs of TTL family (7400 series) in the undergraduate laboratories will soon become obsolete. There is a need to prepare our students with the latest developments so to be prepared for the demands of the industry.

The number & variety of Electronic gadgets performing dedicated functions around us are increasing day by day. These dedicated functions are performed by embedded systems. An embedded system is a complete computer system which is embedded in a complete device. Modern embedded systems are often based on microcontrollers.

This Faculty Development Program aims at equipping undergraduate Electronics / Computer Science / Physics teachers with skill and knowledge that are necessary for carrying out experiments in the new CBCS scheme in the area of Digital Electronics, Embedded systems and Microcontrollers.

In this FDP workshop, the teachers will be learning about the Programmable Logic Devices and Embedded system including hands-on activities from competent resource persons.

## About the Institution

*Keshav Mahavidyalaya* has been producing graduates and toppers in various disciplines who have been equipped with knowledge and wisdom. The educational system in the college emphasizes not only on attainment of knowledge and critical reasoning, but also on values and discipline. Our institution believes in the holistic development of the students. This is exemplified by the innumerable laurels achieved by our students in scholastic as well as other domains.

## Department of Electronics

The Department of Electronics offers undergraduate course B.Sc. (H) in Electronics. The objective of the Department is to inculcate not only intellect and creativity among the students through their active engagement in academic and research activities but to help them realize and nurture their extra-academic and extra-curricular disposition as well. The department works for strengthening their foundation which is critical for making them productive members of the society. Each faculty member acts like a mentor and a counselor to the students, guiding them responsibly through their most important phase of life and preparing for their future.



"Only Knowledge can provide salvation"

## FACULTY DEVELOPMENT PROGRAM (National Level Workshop) (UGC Sponsored)\*

## ON EMBEDDED SYSTEM DESIGN USING AVR AND DIGITAL SYSTEM DESIGN USING PROGRAMMABLE LOGIC DEVICES (FDPESCPLD-2017)

1<sup>st</sup> & 2<sup>nd</sup> NOVEMBER 2017



Organized by

**DEPARTMENT OF ELECTRONICS  
KESHAV MAHAVIDYALAYA  
NAAC ACCREDITED INSTITUTION "A" GRADE  
(UNIVERSITY OF DELHI)  
IN COLLABORATION WITH  
CENTRE FOR ELECTRONICS DESIGN AND  
TECHNOLOGY (CEDT), NSIT, DELHI  
Website: [www.keshav.du.ac.in](http://www.keshav.du.ac.in)  
Email: [fdpkmv@gmail.com](mailto:fdpkmv@gmail.com)**

**Venue:  
Keshav Mahavidyalaya,  
H-4-5 Zone, Pitampura,  
Delhi-110034**

\*Approval from UGC awaited

## Principal

**Dr. Madhu Pruthi**

Principal, Keshav Mahavidyalaya

## Coordinator

**Dr. Jagneet Kaur Anand,**

Associate Professor, Department of Electronics

## Organizing Committee, Department of Electronics

**Dr. Vinod Kumar Sharma**

**Dr. Neha Sharma**

**Dr. Jyoti Anand**

**Mr. Himanshu Kushwah**

**Mr. Anil Sethi**

**Dr. Jyoti Bansal**

**Mr. Prashant Kumar**

**Mr. Nitin Kumar**

## Eminent Speaker

**Prof. Dhananjay V. Gadre,**

Director, TI Centre of Embedded Product Design, (Ti-CEPD) and

Coordinator, Centre for Electronics Design and Technology (CEDT),

NSIT, Dwarka, Delhi-110075

**For more details, write to us at**  
[fdpkmv@gmail.com](mailto:fdpkmv@gmail.com)

**Last date of registration**  
22<sup>nd</sup> October, 2017

**Registration and Kit Distribution**  
1st November, 2017, 9:00 AM-9:30 AM

## Format of Registration Form

Full Name\_\_\_\_\_

Designation\_\_\_\_\_

Name of College/Institution\_\_\_\_\_

Name of University\_\_\_\_\_

Email ID\_\_\_\_\_

Mobile\_\_\_\_\_

Address for Correspondence\_\_\_\_\_

## Payment Details

Registration fee of Rs. 1000 (One Thousand rupees Only) can be paid through demand draft in favour of Principal, Keshav Mahavidyalaya, payable at Delhi.

Demand Draft No.\_\_\_\_\_

Dated\_\_\_\_\_

Or through NEFT with the details

Bank: Canara Bank

Branch Address: Rani Bagh, Delhi

IFSC Code: CNRB0002370

Account Name: Keshav Mahavidyalaya

Account No.: 2370101014416

NEFT No.\_\_\_\_\_

Dated\_\_\_\_\_

**Note:**

**Please mention 'FDPESCPLD-2017' in Remarks .**

## Local Hospitality

Registered participants will be provided with free registration kit, lunch and tea/ coffee at the venue. No TA/DA will be provided to the participants. Outstation participants need to arrange their accommodation on their own.

## Registration of Participants

Interested Faculty members from colleges/ universities in India can apply by filling an online registration form along with a registration fee of INR 1000 (One Thousand Rupees Only).

- Limited Seats are available.
- Selection of participants shall be done on first come first serve basis.
- Please email the transaction confirmation details to [fdpkmv@gmail.com](mailto:fdpkmv@gmail.com)
- Participants need to bring copy of transaction details for the issue of registration fee receipt.

## How to Register

Interested Faculty members are required to fill the online registration form available on website of the college [www.keshav.du.ac.in](http://www.keshav.du.ac.in) and on the direct link available here ([Click Here](#))

**Certificate of participation shall be provided to all the participants.**